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SM 1 SM 1 EECE 488 Set 1: Introduction and Background EECE488: Analog CMOS Integrated Circuit Design Introduction and Background Shahriar Mirabbasi

### **EECE488: Analog CMOS Integrated Circuit Design**

In Problem 7. assume  $C_{in} = 0.2$  pF and calculate  $C_1$  and  $C_2$ . 11.17 and 11. Design of Analog CMOS Integrated Circuits Behzad Razavi Errata in Problem Sets Chapter 2 In Eq. change  $M_2$  to  $M_1$ . 3.4.. assume  $I = 0.805$  mA.

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An alternative design that can resolve signals with accuracies much less than the input offset voltage of OpAmps is to use switched capacitors with clocks. Clock is a slightly advanced version of

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